

220. LOW POWER CONSUMPTION USING AVERAGE 8T SENSING SRAM

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Low voltage and Low power SRAM (Static Random Access Memory) design is critical in power concuss devices. To reduce the power in the concuss devices, a new average 8T write /read decoupled SRAM architecture is proposed .The proposed architecture deals with data independent leakage concept and read port that provides robust and faster read operation and reduce concern in half selected cell..The average 8T decoupled SRAM is compared with conventional 8T and simulation results of the proposed provides power consumption than the proposed.

Index Terms - average 8T decoupled SRAM, concuss devices, sub threshold.

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